An *Online* Delay Efficient Packet Scheduler for M2M Traffic in Industrial Automation

Akshay Kumar, Ahmed Abdelhadi and Charles Clancy Hume Center, Virginia Tech Email:{akshay2, aabdelhadi, tcc}@vt.edu

Abstract—Some Machine-to-Machine (M2M) communication links particularly those in a industrial automation plant have stringent latency requirements. In this paper, we study the delay-performance for the M2M uplink from the sensors to a Programmable Logic Controller (PLC) in a industrial automation scenario. The uplink traffic can be broadly classified as either Periodic Update (PU) and Event Driven (ED). The PU arrivals from different sensors are periodic, synchronized by the PLC and need to be processed by a prespecified firm latency deadline. On the other hand, the ED arrivals are random, have lowarrival rate, but may need to be processed quickly depending upon the criticality of the application. To accommodate these contrasting Quality-of-Service (QoS) requirements, we model the utility of PU and ED packets using step function and sigmoidal functions of latency respectively. Our goal is to maximize the overall system utility while being proportionally fair to both PU and ED data. To this end, we propose a novel online QoS-aware packet scheduler that gives priority to ED data as long as that results the latency deadline is met for PU data. However as the size of networks increases, we drop the PU packets that fail to meet latency deadline which reduces congestion and improves overall system utility. Using extensive simulations, we compare the performance of our scheme with various scheduling policies such as First-Come-First-Serve (FCFS), Earliest-Due-Date (EDD) and (preemptive) priority. We show that our scheme outperforms the existing schemes for various simulation scenarios.

Index Terms—M2M, Latency, Quality-of-Service, Scheduling

I. INTRODUCTION

Automation for industrial monitoring and control processes has become commonplace these days. It can be modeled as a star-topology Machine-to-Machine (M2M) network of various process sensors communicating to a Programmable Logic Controller (PLC) on the 'uplink channel' which then feeds back the output to a control device ('downlink channel') to ensure the desired process operation. Due to increasing complexity and scale of modern industrial monitoring and control systems, there has been a paradigm shift from the use of point-to-point wired systems to wireless technologies for both uplink and downlink communication. Wireless technologies specifically tailored for signaling in industrial monitoring systems, such as WISA, Zigbee Pro etc. (which are based on IEEE 802.15.1 and IEEE 802.15.4 standard) are becoming increasing popular (see [1] and references therein).

Typically, the industrial M2M traffic has very low latency requirements (of order of few milliseconds). It can be broadly

This research is based upon work supported by the National Science Foundation under Grant No. 1134843.

categorized as either *non real-time* (no deadline for task completion) or *soft real-time* (decreased utility if deadline not met) or *firm* (zero utility if deadline not met) or *hard* (system failure if deadline not met). For instance, the messages from a instrumented protective system or safeguarding system have *hard* real-time Quality-of-Service (QoS) requirements whereas preventive maintenance applications are typically served in *non real-time*. With increase in network size, the available computational and (wireless) communication resources gets shared among a large number of sensors. This makes it harder to provide real-time QoS due to simultaneous access attempts from multiple sensors [2] and also wastage of wireless spectrum continuously allocated to sensors with very low transmission duty cycle [3].

Therefore in this paper, we develop an *online* delay-efficient packet scheduler for the M2M traffic. To begin, we first use source M2M traffic model in [4] to classify the sensor data into Periodic Update (PU) and Event Driven (ED) packets. The PU arrivals are periodic with *firm*¹ latency deadline while the ED arrivals are random without hard service deadlines.

Paper Contributions: We map the contrasting QoS requirements for PU and ED packets using step and sigmoidal utility functions. We define the overall system utility as the product of (time) averaged PU and ED utilities so as to ensure proportional fairness between the two classes. Our goal is to determine the optimal scheduling policy that maximizes the proposed system utility. However, due to the randomness in arrival and service times, there exists no delay-optimal *online* algorithm [5].

Therefore, we propose an *online* delay-efficient heuristic scheduler for the uplink traffic at the PLC, that gives priority to ED data as long as that does not result in a utility loss for the PU data. This results in a higher utility for the ED data. However, at high server utilization, as the PU task start failing their task completion deadlines, we propose to drop such PU packets as their completion does not result in utility. This helps us to mitigate overall congestion and thus allows us to meet the deadlines of remaining PU tasks and also serve the

¹We assume *firm* QoS for PU instead of *hard* QoS because typically the periodic data is a slowly varying function of time and will not change drastically from one period to another. So if we don't meet the completion deadline for a PU task, the PLC can use an estimate for the PU based on the recent history, albeit with some degradation in utility. The estimate will typically be better if we store more historical PU data but this increase the memory size and computational complexity.

ED data with higher utility. Using extensive simulations, we show that this results in an overall increase in system utility as compared to other popular scheduling policies such as First-Come-First-Serve (FCFS), Earliest Due Date (EDD), priority scheduling etc. Lastly, the proposed scheduler is agnostic to the wireless technology being used for the M2M uplink and is also independent of the hardware-software architecture used in practical real-time embedded systems. Also it can easily adapt to accommodate time-varying arrival and service rates for the PU and ED packets.

Paper Outline: The rest of the paper is organized as follows. Section II details the related work. Section III introduces the system model and defines the utility functions for PU and ED data. Then in Section IV, we formulate the utility maximization problem and in Section V describe the proposed scheduling scheme at PLC. Section VI presents simulation results. Finally Section VII draws some conclusions.

II. RELATED WORK

Over the last decade, numerous hardware and software technologies including embedded real-time systems, Ethernet, Low-power Wi-Fi, Zigbee have made their way into industrial automation platforms so as to increase their flexibility and efficiency. However, this has resulted in increased complexity both at the network and component level, due to sharing of physical resources for various applications. This makes it harder to guarantee QoS for (hard) real-time applications. It was shown by Buttazzo in [6] that merely increasing the computational resources is not too useful unless appropriate scheduling strategies are put in place. Consequently, a number of prior works have looked into integrating the design of realtime scheduling schemes in middle-ware (such as CORBA) based architectures. Tommaso et. al. in [7] presented a realtime Service Oriented Architecture (SOA) for industrial automation. However, all of these works are mostly tangential to our line of work in the sense that our proposed scheduler is agnostic of the hardware-middleware-software architecture and maps the real-time QoS requirements of M2M traffic into utility functions that we aim to maximize.

Another line of work focuses on QoS-aware packet scheduler for M2M traffic in Long Term Evolution (LTE) network (see [8] and references therein). Most of these works use some variants of Access Grant Time Interval scheme for allocating fixed or dynamic access grants over periodic time intervals to M2M devices. Nusrat et. al. in [9] designed a packet scheduler for M2M in LTE so as to maximize the percentage of uplink packets that satisfy their individual budget limits. Ray and Kwang in [10] proposed a distributed congestion control algorithm which allocates rates to M2M flows in proportion of their demands. Unlike our work, all of these works design packet scheduler specific to a wireless standard such as LTE and are thus heavily influenced by the Medium Access Control (MAC) architecture of LTE. Unlike our work, they also don't explicitly segregate data arrivals into different QoS classes.

Lastly, a number of scheduling algorithms have been proposed specifically for real-time embedded systems (see [11]

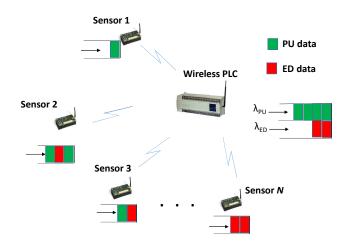


Fig. 1. System Model.

and references therein) that are agnostic to the application scenario, wireless technology used or hardware-software architecture. These schemes assume hybrid task sets comprising of hard periodic requests and soft real-time aperiodic requests. They are broadly classified into Fixed-priority and Dynamic priority assignments. The goal of all these schemes is to guarantee completion of service (before deadline) for all periodic request and simultaneously aim to reduce the average response times of aperiodic requests. Fixed priority schemes schedule periodic tasks using Rate-Monotonic algorithm but differ in service for aperiodic tasks. Dynamic scheduling algorithms schedule periodic tasks using EDD scheme and allow better processor utilization and enhance aperiodic responsiveness as compared to the fixed priority schemes. The drawbacks of these schemes is that they assume that the random service times for each tasks are known apriori. Hence these schemes are not truly online in their present form and would require considerable modifications.

III. SYSTEM MODEL

Fig 1 shows the system model illustrating the queuing process in the uplink channel (i.e., from sensors to the PLC) in an industrial automation setting. Each of the N sensors transmits two types of packets to the PLC: frequent periodic updates (PU) for some process related measurement or sporadic event-driven (ED) packets triggered when the sensor measurement for a physical quantity crosses its threshold. The PU arrivals from the $i^{\rm th}$ sensor is modeled as a deterministic periodic process with period $T^i_{\rm pu}$ while the ED arrivals is modeled as a Poisson process with rate $\lambda^i_{\rm ed}$. Therefore the queuing process at the PLC has net arrival rate $\lambda_{\rm pu}$ and $\lambda_{\rm ed}$ for PU and ED packets respectively, where

$$\lambda_{\text{pu}} = \sum_{i=1}^{N} 1/T_{\text{pu}}^{i} \text{ and } \lambda_{\text{ed}} = \sum_{i=1}^{N} \lambda_{\text{ed}}^{i}.$$
 (1)

Let s_{pu} and s_{ed} denote the size for PU and ED packets. The service time for PU and ED packets is assumed to be exponential with rate μ_{pu} and μ_{ed} respectively given by

$$\mu_{\rm pu} = \mu/s_{\rm pu}$$
 and $\mu_{\rm ed} = \mu/s_{\rm ed},$ (2)

where μ is the service rate at PLC per unit packet size. In general, the total time spent by a packet in the system, T, can be written as sum of following components,

$$T = T_{\text{trans}} + T_{\text{prop}} + T_{\text{cong}} + T_{\text{queue}} + T_{\text{ser}}, \tag{3}$$

which denote the following component delays:

- T_{trans} : Transmission delay at the sensor.
- T_{prop} : Propagation delay from the sensor to PLC.
- T_{cong} : Congestion delay due to shared wireless channel in large-scale sensor network.
- T_{queue} : Queuing delay at the PLC.
- T_{ser} : Processing time for a packet at the PLC.

In this work, we ignore all the terms except the queuing delay at PLC $T_{\rm queue}$ and the service time $T_{\rm ser}$. This is because the packet size is usually small enough to ignore $T_{\rm trans}$ and the sensors and PLC are usually close enough on the shop floor to permit the usage of low power wireless signaling and thus ignore $T_{\rm prop}$. Also, we assume that the available wireless spectrum is large enough to allocate a dedicated transmission channel to each sensor; so there is no congestion delay $T_{\rm cong}$.

At the PLC, the PU and ED packets form two separate queues due to different attributes such as packet size and latency requirements. However, for simplicity for a given PU or ED class, we don't distinguish between packets arriving from different sensors. Now the queuing delay for each class depends upon the scheduling policy adopted at the PLC. The scheduling policy at PLC should be chosen as to maximally satisfy the latency constraints of PU and ED packets.

IV. PROBLEM FORMULATION

We first map the latency requirements onto the utility functions for both PU and ED classes as shown in Fig. IV-B and IV-B.

A. Utility Functions

1) PU utility: As stated previously, the PU packets need to be processed within a prespecified (usually by PLC) time interval at the end of which there is no utility in serving the packet. So, we define PU utility as,

$$U_{pu}(l) = \begin{cases} 1 & \text{if } l < l_d \\ 0 & \text{if } l \ge l_d, \end{cases} \tag{4}$$

where l_d is the latency deadline at which utility drops to 0.

2) ED utility: Unlike, the PU packets, ED packets do not have a hard deadline, rather their utility is a strictly decreasing function of latency. However the utility does not decrease much until some nominal delay which depends on criticality of application. Also at large latency values, the utility becomes close to 0 and does not change appreciably with further increase in latency. Therefore, we define the ED utility as a sigmoidal function, as in [12], [13], given by,

$$U_{\rm ed}(l) = 1 - c \left(\frac{1}{1 + e^{-a(l-b)}} - d \right),$$
 (5)

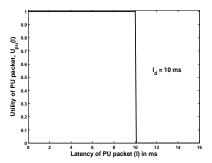


Fig. 2. Utility function for PU packet.

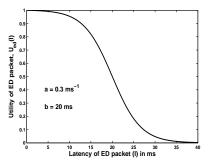


Fig. 3. Utility function for ED packet.

where, $c=\frac{1+\mathrm{e}^{ab}}{\mathrm{e}^{ab}}$ and $d=\frac{1}{1+\mathrm{e}^{ab}}$. We note that $U_{\mathrm{ed}}(0)=1$ and $U_{\mathrm{ed}}(\infty)=0$. The parameter a is the utility roll-off factor whose value depends on the criticality of the application. The inflection point of (5) occurs at l=b and depends on the average service time (at PLC) for ED packet.

B. System utility function

For a given scheduling policy \mathcal{P} , we use utility proportional fairness in our system utility (similar to the system utility introduced in [14], [15]) which is given by,

$$V(\mathcal{P}) = \mathbf{U}_{\text{pu}}^{\beta_{\text{pu}}}(\mathcal{P}) * \mathbf{U}_{\text{ed}}^{\beta_{\text{ed}}}(\mathcal{P}), \tag{6}$$

where $\mathbf{U}_{pu}(\mathcal{P})$ and $\mathbf{U}_{ed}(\mathcal{P})$ are the utility of PU and ED packets in the steady state given as,

$$\mathbf{U}_{pu}(\mathcal{P}) = \lim_{T_s \to \infty} \sum_{i=1}^{M_{pu}(T_s)} \frac{U_{pu}(l_i(\mathcal{P}))}{M_{pu}(T_s)},\tag{7}$$

$$\mathbf{U}_{\mathrm{ed}}(\mathcal{P}) = \lim_{T_{\mathrm{s}} \to \infty} \sum_{i=1}^{M_{\mathrm{ed}}(T_{\mathrm{s}})} \frac{U_{\mathrm{ed}}(l_{i}(\mathcal{P}))}{M_{\mathrm{ed}}(T_{\mathrm{s}})},\tag{8}$$

where $M_{\rm pu}(T_{\rm s})$ and $M_{\rm ed}(T_{\rm s})$ are the number of PU and ED packets served in time $T_{\rm s}$ and l_i is the latency of the $i^{\rm th}$ packet. The parameters $\beta_{\rm pu}$ and $\beta_{\rm ed}$ denote the relative importance of average PU and ED utility. A higher value of β implies higher importance of the utility of that class towards the overall system utility.

C. Optimal scheduler

We now describe the utility maximization problem that needs to be solved to determine the optimal scheduling policy at PLC. It is given by,

$$\max_{\mathcal{P}} V(\mathcal{P}) = \mathbf{U}_{pu}^{\beta_{pu}}(\mathcal{P}) * \mathbf{U}_{ed}^{\beta_{ed}}(\mathcal{P}). \tag{9}$$

If the service times of PU and ED were deterministic or known *apriori*, then the optimal scheme is to schedule PU jobs so that they are completed exactly at the deadline l_d . However, since the arrival time of ED packet, the service times for PU and ED packets are random, it is not possible to determine an *online* optimal scheduler [5]. Therefore, we propose an *online* heuristic scheduler that aims to maximize the utility function and is described in the next section.

V. PROPOSED SCHEDULER

The utility of PU remains at 1 until the deadline at which it becomes 0. Therefore, it would be best (from the perspective of ED latency) to delay the service of PU as much as possible but ensuring that we serve it before deadline. However, the randomness in PU and ED service times makes it hard to determine the optimal start of service for each PU packet in real-time. To overcome this problem, our proposed scheduler gives priority to ED packets as long as the current latency of PU packet is less than predetermined threshold l_t , (0 < $l_t < l_d$). If the latency of PU packet exceeds l_t and PLC is processing an ED packet, then it gets preempted and the PU packet gets service. This ensures that we reduce latency of ED packets while ensuring that most of the PU packets meet their latency deadline. As we will show later, there exists an optimal value of l_t that maximizes the system utility for given set of system parameters. Using simulations, the optimal l_t can be stored in a Look-Up-Table (LUT) prior to the deployment of proposed scheduler. Thus the proposed scheme can easily adapt to changing packet arrival rate, packet size, number of sensor nodes etc. by looking up and using the optimal l_t for each case.

Another novel feature of our scheduler is that we drop PU packets from service or queue that exceed their latency deadline as there is no resultant utility from servicing such packets. However, dropping them from service will reduce congestion and thus reduce latency for PU and ED packets in queue. The proposed scheduler is described in detail in Algorithm 1.

VI. SIMULATION RESULTS

In this section, we use Monte-Carlo simulations to evaluate the system utility performance of our scheduler against various standard scheduling policies such as FCFS, EDD, Preemptive priority scheduling. The simulation time T_s is set to a large value (40 s) to ensure a steady-state queuing behavior. N is set to 50 sensors. For simplicity, we set the PU arrival period from each of the N sensors, $T_{\rm pu}^i=50$ ms. The ED arrival rate, $\lambda_{\rm ed}^i$ is same for all sensors and set to $0.0068/{\rm ms}$. PLC service rate is $\mu=100$ bits/ms. The relative importance of PU and ED is set equal, i.e. $\beta_{\rm pu}=\beta_{\rm ed}=1$, unless mentioned otherwise.

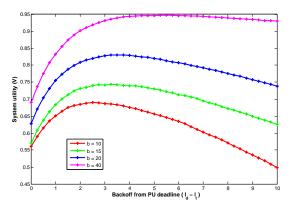


Fig. 4. Selection of optimal PU latency threshold l_t .

The size of PU and ED packets is set to $s_{\rm pu}=100$ bits and $s_{\rm ed}=200$ bits respectively and does not change over time or different sensors. The latency deadline for PU, l_d is set to 10 ms for all sensors. For ED utility function, we set $a=1/{\rm ms}$ and b=20 ms unless mentioned otherwise.

A. Impact of latency threshold and ED utility parameter, b

Fig. 4 illustrates the impact of varying the PU latency threshold l_t in proposed scheduler (without PU drop) for different values of parameter b. We note that for a given b, there exists an optimal value of l_t that maximizes the system utility. However, at higher values of b, the system utility becomes less sensitive to l_t value around the maxima. This is because the ED utility does not vary appreciably with latency when b is high; and PU utility does not vary much at low values of l_t .

B. Impact of ED utility parameter, a

Fig. 5 illustrates the impact of varying the ED utility parameter a on the system utility for different scheduling policies. As a is increased, ED utility function more brickwalled with the step at latency of b. We observe that at very low value of a, system utility is very high for the proposed schemes. This is because ED utility remains constant with increase in latency. As a is increased, system utility decreases, attains a minima, increases and then settles down. This is because at high a, the ED utility becomes a brick-wall function and any further increase in a does not affect system utility.

C. Impact of number of sensor nodes

Fig. 6 shows the impact of increasing number of sensors on the system utility for different scheduling policies. For proposed scheduler, we run Algorithm 1 over $0 < l_t < l_d$ and determine the optimal l_t that results in maximum utility V. Clearly, the proposed scheduler (irrespective of dropping PU packets) always results in a higher system utility compared to all other schemes. However the performance improvement is starkly better as N is increased. This is because at large N, the latencies of PU and ED becomes more interdependent and order of service for queued jobs becomes quite important.

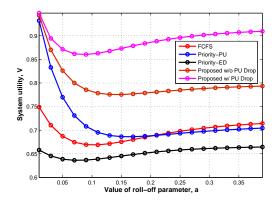


Fig. 5. Impact of criticality of ED application set by parameter a.

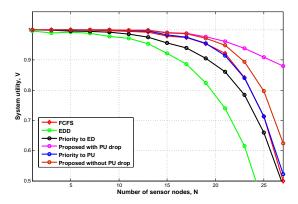


Fig. 6. System utility as network size is increased.

Also at large N, the proposed scheme with PU drop results in higher dropped packets and thus relieves congestion and results in a significantly higher utility as compared to other schemes.

D. Impact of ED arrival rate

Fig. 7 shows the impact of increasing the frequency of ED arrivals for a given network. Again for similar reason as stated earlier, we note that the proposed scheduler results in higher utility compared to other schemes.

VII. CONCLUSIONS

In this paper, we presented a delay-efficient packet scheduler for uplink M2M traffic. To this end, we classified the uplink traffic as PU or ED packets and mapped their latency requirements onto utility functions. We then proposed a novel packet scheduler to maximize the system utility. We defined the system utility as the weighted product of average PU and ED utility so as to ensure proportional fairness. Using extensive simulations, we did a comparative analysis of the proposed scheme with standard scheduling policies such as FCFS, EDD, Preemptive priority etc. We note that as the network size increases or ED arrival rate increases, there is a stark difference between the utility of proposed scheme and the rest.

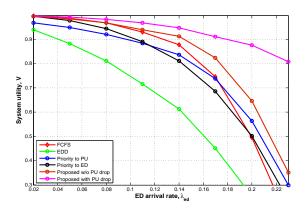


Fig. 7. System utility as ED arrival rate is increased.

REFERENCES

- P. Zand, S. Chatterjea, K. Das, and P. Havinga, "Wireless industrial monitoring and control networks: the journey so far and the road ahead," *Journal of sensor and actuator networks*, vol. 1, pp. 123–152, August 2012.
- [2] 3GPP, "Study on RAN Improvements for Machine-type communications," tech. rep., Technical report, TR 37.868, 2012.
- [3] D. Drajic, S. Krco, I. Tomic, M. Popovic, N. Zeljkovic, N. Nikaein, and P. Svoboda, "Impact of online games and m2m applications traffic on performance of hspa radio access networks," in *International Conference* on *Innovative Mobile and Internet Services in Ubiquitous Computing*, pp. 880–885, July 2012.
- [4] N. Nikaein, M. Laner, K. Zhou, P. Svoboda, D. Drajic, M. Popovic, and S. Krco, "Simple traffic modeling framework for machine type communication," in *International Symposium on Wireless Communication* Systems, pp. 1–5, Aug 2013.
- [5] T. Tia, J. Liu, and M. Shankar, "Algorithms and optimality of scheduling soft aperiodic requests in fixed-priority preemptive systems," *Real-Time* Systems, vol. 10, pp. 23–43, Jan 1996.
- [6] G. Buttazzo, Hard Real-Time Computing Systems: Predictable Scheduling Algorithms and Applications (Real-Time Systems Series). Santa Clara: Springer-Verlag TELOS, 2004.
- [7] T. Cucinotta, A. Mancina, G. Anastasi, G. Lipari, L. Mangeruca, R. Checcozzo, and F. Rusina, "A real-time service-oriented architecture for industrial automation," *IEEE Transactions on Industrial Informatics*, vol. 5, pp. 267–277, Aug 2009.
- [8] A. Gotsis, A. Lioumpas, and A. Alexiou, "Evolution of packet scheduling for machine-type communications over lte: Algorithmic design and performance analysis," in *IEEE Globecom Workshop*, pp. 1620–1625, Dec 2012.
- [9] N. Afrin, J. Brown, and J. Khan, "A delay sensitive lte uplink packet scheduler for m2m traffic," in *IEEE Globecom Workshop*, pp. 941–946, Dec 2013.
- [10] R. Lam and K.-C. Chen, "Congestion control for m2m traffic with heterogeneous throughput demands," in *IEEE Wireless Communications* and Networking Conference, pp. 1452–1457, April 2013.
- [11] G. Buttazzo, Hard Real-Time Computing Systems: Predictable Scheduling Algorithms and Applications. New York: Springer, Third ed., 2011.
- [12] M. Ghorbanzadeh, A. Abdelhadi, and T. Clancy, "A utility proportional fairness bandwidth allocation in radar-coexistent cellular networks," in *Military Communications Conference (MILCOM)*, 2014.
- [13] A. Abdelhadi and C. Clancy, "An optimal resource allocation with joint carrier aggregation in 4G-LTE," in *Computing, Networking and Communications (ICNC)*, 2015 International Conference on, pp. 138–142, Feb 2015.
- [14] A. Abdelhadi, T. Clancy, and J. Mitola, "A Resource Allocation Algorithm for Users with Multiple Applications in 4G-LTE," in ACM Workshop on Cognitive Radio Architectures for Broadband (MobiCom Workshop CRAB), 2013.
- [15] M. Ghorbanzadeh, A. Abdelhadi, and C. Clancy, "Optimal radio resource allocation for hybrid traffic in cellular networks: Centralized and distributed architecture," CoRR, vol. abs/1411.4011, 2014.

```
Algorithm 1 Proposed Online Packet Scheduler
```

```
Inputs
l_d and l_t are latency deadline and threshold for PU utility such that 0 < l_t < l_d
U_{\rm pu}(.) and U_{\rm ed}(.) are utility functions for PU and ED packets
\beta_{pu} and \beta_{ed} are parameters for system utility in (6)
T_{\rm pu}^a and T_{\rm ed}^a are arrays for arrival times of PU and ED packets
T_{\rm pu}^s and T_{\rm ed}^s are arrays for service times of PU and ED packets
Outputs
V is optimal policy
Local
T_1, T_2, T_3 store current time, service completion time and next arrival time
x, z is type of next packet to be served and the current packet in server. Set them to 1 for PU and 2 for ED packet
p_1, p_2 are pointers to the next PU and ED packet to be served
T_{pu}^d and T_{ed}^d are arrays for completion times of PU and ED packets
D_{pu} and D_{ed} are arrays for latency of PU and ED packets
U<sub>pu</sub> and U<sub>ed</sub> are average utilities for PU and ED packets
r_1, r_2 are sizes of arrays T_{\rm pu}^a and T_{\rm ed}^a. Set \epsilon to infinitesimally small value.
Initialization
p_1 = 1, p_2 = 1, T_1 = 0, T_2 = 0
Begin Algorithm:
   while p_1 \leq r_1 or p_2 \leq r_2 do
       if T_{pu}^a(p_1) \ll T_{ed}^a(p_2) then
                                                                                                   Determine whether next arrival is PU or ED
           T_3 = T_{\text{pu}}^a(p_1), x = 1
                                                                                                    ▶ Set next arrival time and packet type to PU
            T_3 = T_{\rm ed}^a(p_2), x = 2
                                                                                                   ▶ Set next arrival time and packet type to ED
       end if
       if T_1 \leq T_3 then
            T_1 = T_3
                                                                                               ▶ Advance current time to next PU or ED arrival
       else if z=2 and T_2>T^a_{\rm pu}(p_1)+l_t then
                                                                                               \triangleright Preempt active ED job if PU latency exceeds l_t
            p_2 = p_2 - 1, z = 1
           T_{\text{ed}}^{s}(p_2) = T_2 - (T_{\text{pu}}^{a}(p_1) + l_t)
                                                                                                   ▶ Residual service time for preempted ED job
           T_1 = T_{\text{pu}}^a(p_1) + l_t
                                                                                                     > Retreat current timer to preemption instant
       else if T_1 > \max(T_{pu}^a(p_1), T_{ed}^a(p_2)) then
                                                                                              ▶ Check if both PU and ED jobs present in queue
           if T_1 > T_{\text{pu}}^a(p_1) + l_t then x = 1
                                                                                                   \triangleright Service PU job first if its latency exceeds l_t
            else
                 x = 2
            end if
       end if
                                                                                  ▷ Update the timers and pointers for next PU or ED packet
            z = 1, T_2 = T_1 + T_{pu}^s(p_1), T_{pu}^d(p_1) = T_2, p_1 = p_1 + 1
            z = 2, T_2 = T_1 + T_{ed}^s(p_2), T_{ed}^d(p_2) = T_2, p_2 = p_2 + 1
       if z = 1 and T_2 > T_{pu}^s(p_1 - 1) + l_d then
                                                                                    Drop the PU packet with latency exceeding the deadline
           T_{\text{pu}}^d(p_1-1) = T_{\text{pu}}^a(p_1-1) + l_d + \epsilon, T_1 = \max(T_1, T_{\text{pu}}^a(p_1-1) + l_d)
            T_1 = T_2
                                                                                                 ▶ Advance the timer to service completion time
       end if
  D_{pu} = T_{pu}^d - T_{pu}^a, \ D_{ed} = T_{ed}^d - T_{ed}^a
                                                                                                       ▶ Calculate latency for PU and ED packets
   \mathbf{U}_{\mathrm{pu}} = \mathrm{mean}\left(U_{\mathrm{pu}}(L_{\mathrm{pu}})\right), \ \mathbf{U}_{\mathrm{ed}} = \mathrm{mean}\left(U_{\mathrm{ed}}(L_{\mathrm{ed}})\right), \ V = \mathbf{U}_{\mathrm{pu}}^{\beta_{\mathrm{pu}}} * \mathbf{U}_{\mathrm{ed}}^{\beta_{\mathrm{ed}}}
                                                                                                            Determine PU, ED and system utility
```